



INDUJA M K

Assistant Professor

Department of Electronics and Communication Engineering
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Educational Background

- MTech in Micro and Nano Electronics from College of Engineering Trivandrum (2026)
 - BTech in Electronics and Communication Engineering from College of Engineering Trivandrum (2020)
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Area of Interest

- VLSI Design
- FPGA based systems
- Low power VLSI
- Digital system design
- RTL Design

Area of Expertise

- VLSI Design
- FPGA-Based Digital System Design
- RTL Design
- Digital Circuit Design
- Low-Power VLSI

- Memory System Architecture
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Hobbies

- Travelling
- Listening to Music
- Trying New Things
- Exploring diverse cuisines and experiencing regional cultures.

For professional inquiries or collaborations, contact via email:
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